



Docket No.: 0756-7214

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Shunpei Yamazaki et al.

Application No.: 10/694,803

Filed: October 29, 2003

For: SEMICONDUCTOR DEVICE AND

MANUFACTURING METHOD THEREOF,

)

)

) Examiner: Unknown

) Group Art Unit:

) Not Yet Assigned

)

VERIFICATION OF TRANSLATION

Honorable Commissioner of Patents and Trademarks

Washington, D.C. 20231

Sir:

I, Yui Minato, C/O Semiconductor Energy Laboratory Co., Ltd. 398, Hase, Atsugi-shi, Kanagawa-ken 243-0036 Japan, a translator, herewith declare:

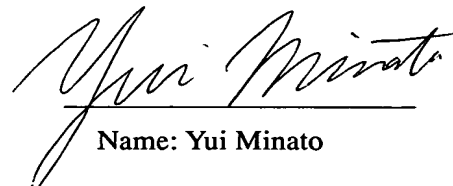
that I am well acquainted with both the Japanese and English Languages;

that I am the translator of the attached translation of the US Patent Application No. 10/694,803 filed on October 29, 2003; and

that to the best of my knowledge and belief the followings is a true and correct translation of the US Patent Application No. 10/694,803 filed on October 29, 2003.

I further declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

Date: this 21<sup>th</sup> day of June 2004

  
Name: Yui Minato